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Electronic Version v1.1

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**Title of
Invention****SYSTEM AND METHOD FOR IMPLEMENTING SELF-TIMED
DECODED DATA PATHS IN INTEGRATED CIRCUITS**

Application Number :

Date :

First Named Applicant: Darren L. Anand

Confirmation Number:

Attorney Docket Number: BUR920020102US1

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Submitted By:	Elec. Sign.	Sign. Capacity
Robert A Walsh Esq. Registered Number: 26,516	Robert A. Walsh /s/	Attorney

Documents being submitted:	Files
us-assignment	BUR920020102US1-usassn.xml us-assignment.xml us-assignment.dtd BUR920020102assignment1.tif BUR920020102assignment2.tif
us-request	BUR920020102US1-usrequ.xml us-request.dtd us-request.xml
us-fee-sheet	BUR920020102US1-usfees.xml us-fee-sheet.xml us-fee-sheet.dtd
application-body	BUR920020102SPEC-trans.xml us-application-body.xml application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920020102figure.tif BUR920020102drawing1.tif BUR920020102drawing2.tif BUR920020102drawing3.tif
us-declaration	BUR920020102Dec1.tif
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us-declaration	BUR920020102Dec3.tif
Comments	
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